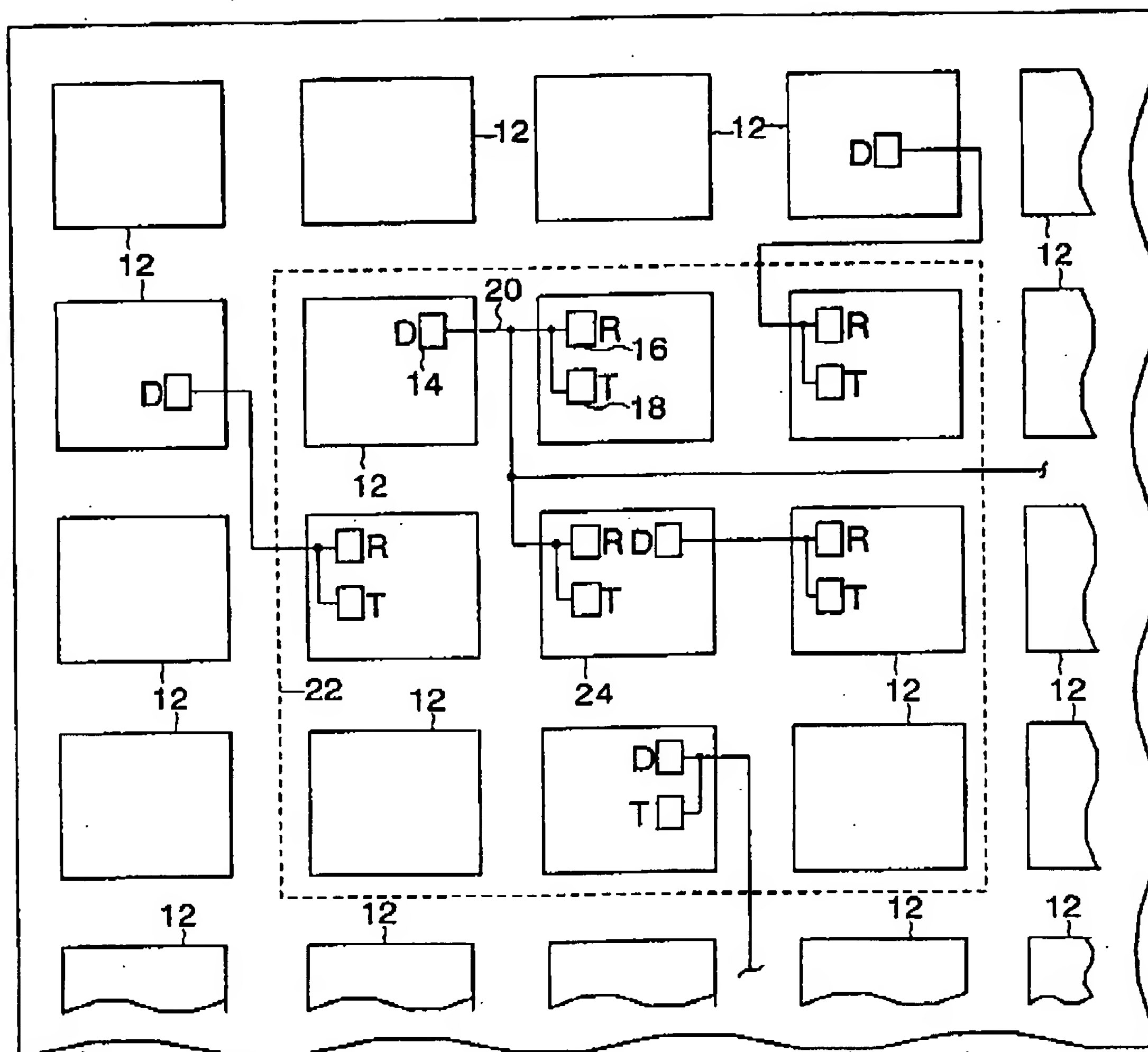| SUBMISSION OF PROPOSED DRAWING AMENDMENT FOR APPROVAL BY EXAMINER (37 CFR 1.121(a) or 37 CFR 1.121(b)(3)(ii))	Docket No. BUR920010207US1

In Re Application Of: **KERRY BERNSTEIN**

Application No. 10/604,179	Filing Date 06/30/2003	Examiner LIN, SUN J.	Customer No. 024241	Group Art Unit 2825	Confirmation No. 1178

Invention: **METHODOLOGY FOR FIXING Qcrit AT DESIGN TIMING IMPACT**

Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attached please find:

(check applicable items)

☐   a sketch in permanent ink

☒   a copy of the original drawing(s) with red ink

showing proposed changes to the drawing(s) in this application for which the approval of the examiner is requested.

Signature

Richard A. Henkler, Reg. No. 39220
IP Law Department, Mail Stop 972E
IBM Corporation
1000 River Street
Essex Junction, VT 05452
Telephone: 802-769-8585

Dated: 12-17-04

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on

12/17/2004.   703-872-9306
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Signature of Person Mailing Correspondence

Typed or Printed Name of Person Mailing Correspondence
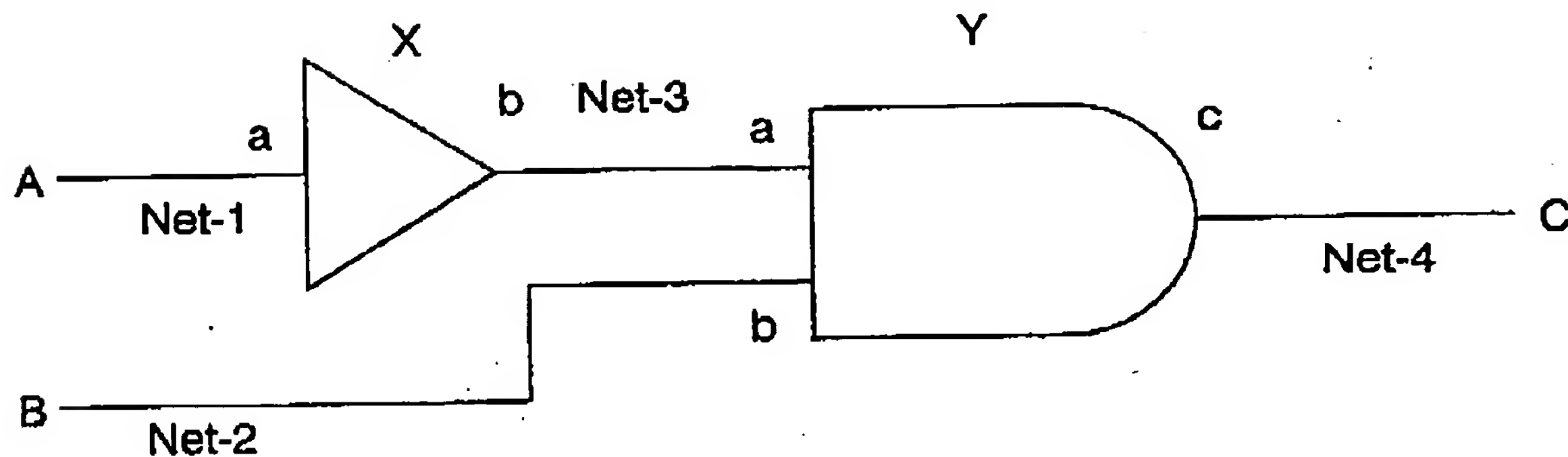
Kerry Bernstein, et al.
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Figure 1 (Prior Art)

X   b   Net-3   a   Y   c

A ————   a   C

Net-1   Net-4

B

Net-2

EXAMPLE OF LOGIC CIRCUIT

Figure 2   (Prior Art)

Port:
  INPUT : A, B:
  OUTPUT: C
EndPort
Net:
  Net-1: pA, Xpa;
  Net-2: pB, Ypb;
  Net-3: Xpb, Ypa;
  Net-4; pC, Ypc;
EndNet;

EXAMPLE OF NET LIST

Figure 3   (Prior Art)